OIPE JC48
OCT 22 2004
PATENT & TRADEMARK OFFICE

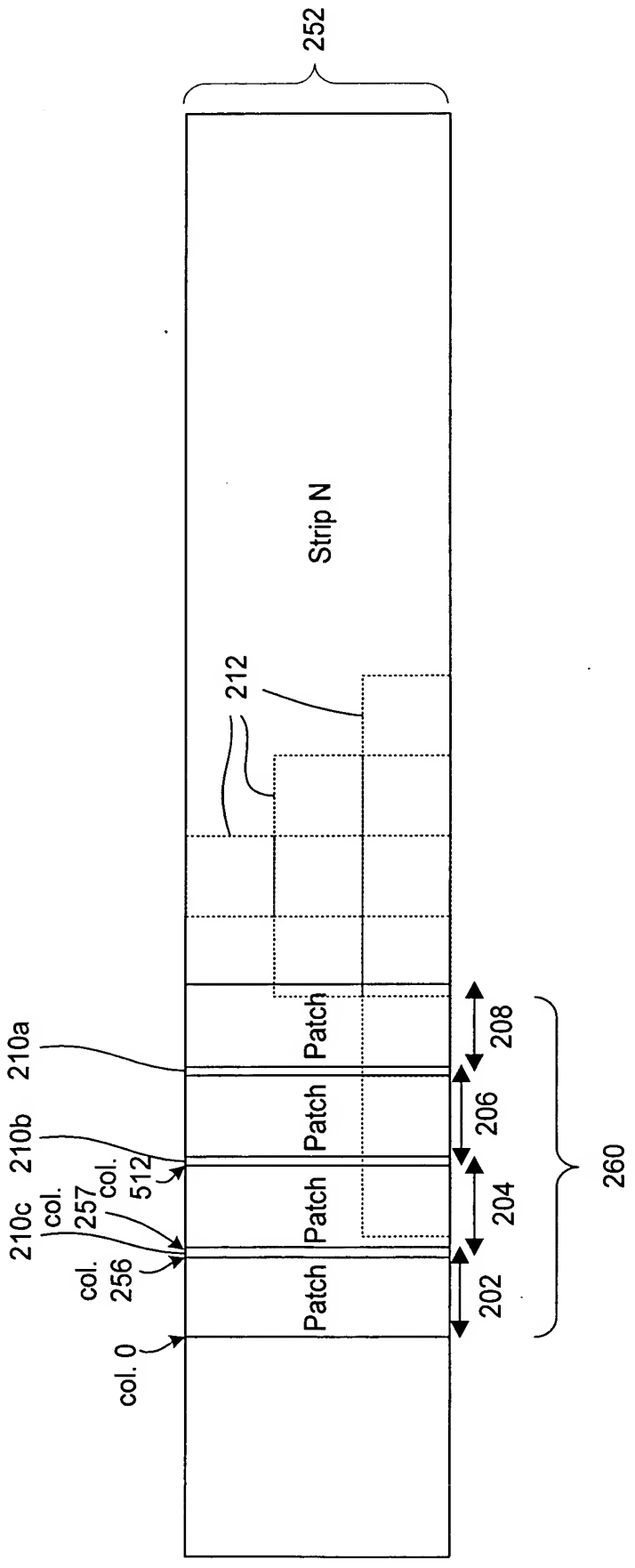

Figure 3

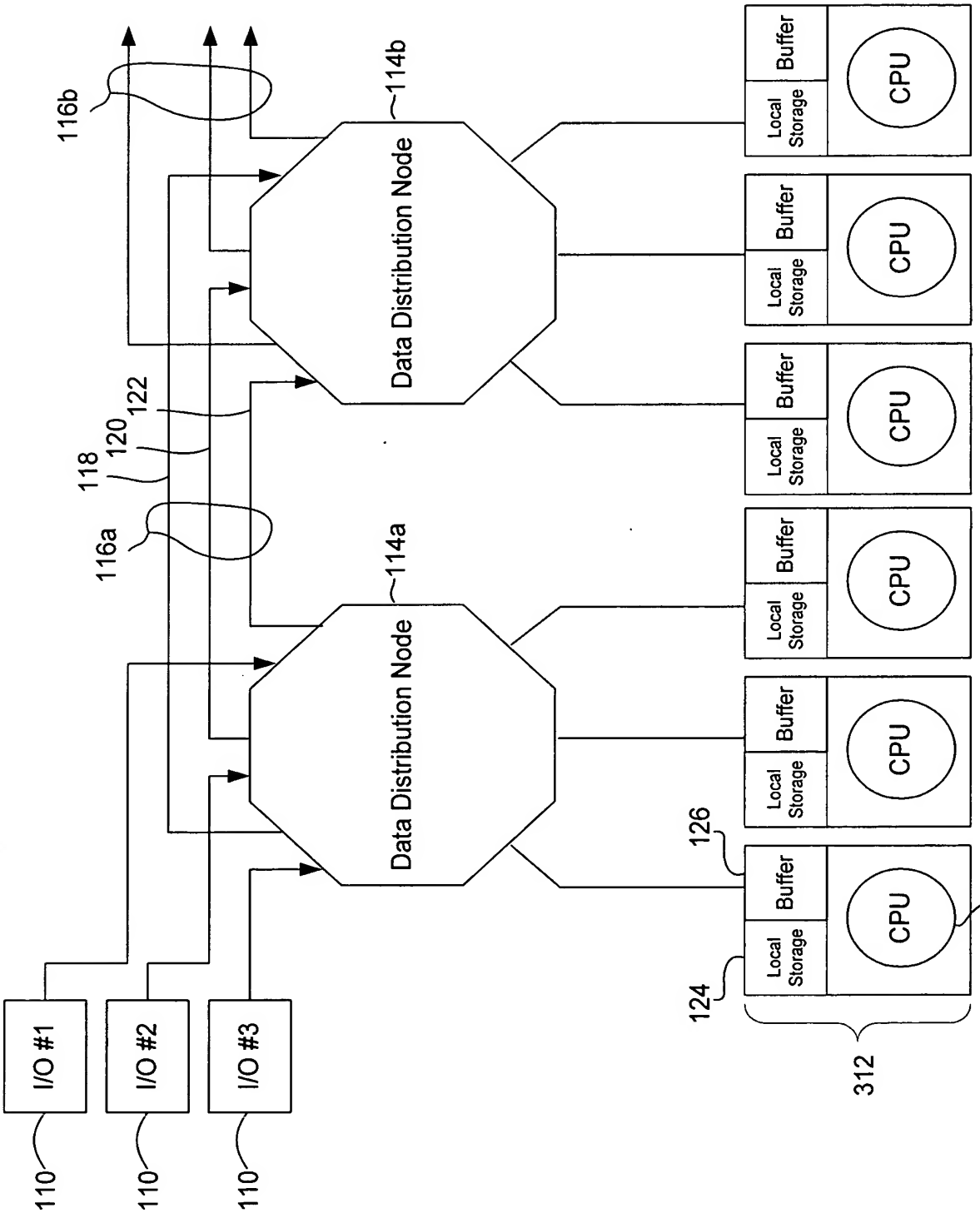
I/O #1
I/O #2
I/O #3
110
110
110
116a
116b
118
120
122
Data Distribution Node
Data Distribution Node
114a
114b
126
124
128
312
Local Storage
Buffer
CPU

FIG. 5